**LIST OF REFERENCES**

[1] “OpenCV.” Wikipedia. Online.   
<http://en.wikipedia.org/wiki/OpenCV>.

[2] “Machine vision.” Wikipedia. Online.   
<http://en.wikipedia.org/wiki/Machine_vision>.

[3] “System on a chip.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/System\_on\_a\_chip.

[4] “Direct memory access.” Wikipedia. Online. http://en.wikipedia.org/wiki/Direct\_memory\_access.

[5] “Field-programmable gate array.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Field-Programmable\_Gate\_Array.

[6] “Central processing unit.” Wikipedia. Online. http://en.wikipedia.org/wiki/Central\_processing\_unit.

[7] “ARM architecture.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/ARM\_Architecture.

[8] “Reduced instruction set computing.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/RISC.

[9] “Complex instruction set computing.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Complex\_instruction\_set\_computing.

[10] “Video Graphics Array.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Video\_Graphics\_Array.

[11] “Webcam.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Webcam.

[12] “Ethernet.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Ethernet.

[13] “Universal Serial Bus (USB).” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Universal\_Serial\_Bus.

[14] “USB On-The-Go.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/USB\_On-The-Go.

[15] “Pixel.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Pixels.

[16] “Microprocessor development board.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Development\_board.

[17] “ULPI - The Standard for High-Speed USB PHYs.” Mentor Graphics. Online.   
http://www.mentor.com/products/ip/usb/usb20otg/phy\_interfaces.

[18] “PHY (chip).” Wikipedia. Online.   
http://en.wikipedia.org/wiki/PHY.

[19] “Unshielded twisted pair (UTP).” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Unshielded\_twisted\_pair.

[20] “Semiconductor intellectual property core.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/IP\_Core.

[21] “Network on a chip.” Wikipedia. Online.   
http://en.wikipedia.org/wiki/Network\_on\_chip.

[22] “Arrow SoCKit Evaluation Board.” Rocket Boards Org. 2014. Online. 8 March, 2014  
http://www.rocketboards.org/foswiki/Documentation/ArrowSoCKitEvaluationBoard.

[23] “Cyclone V SoC Hard Processor System.” Altera. Online. http://www.altera.com/devices/fpga/cyclone-v-fpgas/hard-processor-system/cyv-soc-hps.html.

[24] “Dual-Core ARM Cortex-A9 MPCore Processor.” Altera. Online.   
http://www.altera.com/devices/processor/arm/cortex-a9/m-arm-cortex-a9.html.

[25] Standard Microsystems Corporation, "Hi-Speed USB HOST, Device or OTG PHY with ULPI Low Pin Interface," 2007.

[26] Micrel, “KSZ9021RL/RN: Gigabit Ethernet Transceiver with RGMII Support”, October 2009

[27] Terasic Technologies Inc. “SoCKit: User Manual”, 2013

[28] “Logitech HD Webcam C525.” Logitech. Online. <http://www.logitech.com/en-us/product/hd-webcam-c525>

[29] “Secure Digital.” Wikipedia. Online. <http://en.wikipedia.org/wiki/Micro_SD_Card>.

[30] “Linux kernel.” Wikipedia. Online. http://en.wikipedia.org/wiki/Linux\_Kernel.

[31] “Debian.” Wikipedia. Online. http://en.wikipedia.org/wiki/Debian.

[32] “LXDE.” Wikipedia. Online. http://en.wikipedia.org/wiki/LXDE.

[33] “Altera Quartus.” Wikipedia. Online. http://en.wikipedia.org/wiki/Altera\_Quartus.

[34] “Qsys - Altera’s System Integration Tool.” Altera. Online. http://www.altera.com/products/software/quartus-ii/subscription-edition/qsys/qts-qsys.html.

[35] “SoC Embedded Design Suite.” Altera. Online. http://www.altera.com/devices/processor/arm/cortex-a9/software/proc-soc-embedded-design-suite.html.

[36] “Toolchain Working Group.” Linaro Open Source Organization. Online. https://wiki.linaro.org/WorkingGroups/ToolChain.

[37] “GNU Compiler Collection.” Wikipedia. Online. http://en.wikipedia.org/wiki/GNU\_Compiler\_Collection.

[38] “GitHub.” Wikipedia. Online. http://en.wikipedia.org/wiki/Github.

[39] “Win32DiskImager.” Ubuntu Wiki. Online. https://wiki.ubuntu.com/Win32DiskImager.

[40] “PuTTY.” Wikipedia. Online. http://en.wikipedia.org/wiki/PuTTY.

[41] “Face detection.” Wikipedia. Online. http://en.wikipedia.org/wiki/Face\_detection.

[42] “Facial recognition system.” Wikipedia. Online. http://en.wikipedia.org/wiki/Face\_recognition.

[43] “GPU Module Introduction.” OpenCV 2.4.8.0 Documentation. Online. http://docs.opencv.org/modules/gpu/doc/introduction.html.

[44] “Intel® IPP - Open Source Computer Vision Library (OpenCV) FAQ.” Intel Developer Zone. Online. http://software.intel.com/en-us/articles/intel-integrated-performance-primitives-intel-ipp-open-source-computer-vision-library-opencv-faq#performance.

[45] Neuendorffer, Stephen, Thomas Li, and Devin Wang (August 2013) “Accelerating OpenCV Applications with Zynq-7000 All Programmable SoC using Vivado HLS Video Libraries.” *Application Note: Vivado HLS*. Online. http://www.xilinx.com/support/documentation/application\_notes/xapp1167.pdf.

[46] “USB video device class.” Wikipedia. Online. http://en.wikipedia.org/wiki/USB\_video\_device\_class.

[47] “Linux UVC driver and tools.” Ideas On Board Organization. Online. <http://www.ideasonboard.org/uvc/>.

[48] Viola, P., Jones, M.J.: Rapid object detection using a boosted cascade of simple features. In: Proc. CVPR (2001)

[49] Schapire, Robert E.: Explaining Adaboost. Princeton University, Dept. of Computer Science, 35 Olden Street, Princeton, NJ 08540 USA

[50] J. Cho, S. Mrizaei, J. Oberg and R. Kastner, "FPGA-Based Face Detection System Using Haar Classifiers," ACM, Vols. 978-1-60558-410-2/09/02, 2009.

[51] H. Schneiderman and T. Kanade, "Object Detection Using the Statistics of Parts," International Journal of Computer Vision, vol. 56, no. 3, pp. 151-177, 2004.

[52] Theocharides, T., Vijayjrishman, N., Irwin, M.J.: A parallel architecture for hardware face detection. In: Emerging VLSI Technologies and Architectures (2006)